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**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Yasuyoshi YAMADA

Appln. No. 09/435,448

Filed: November 22, 1999



Group Art Unit: 2811

Examiner: Unknown

For: BACK ELECTRODE TYPE ELECTRONIC PART AND ELECTRONIC ASSEMBLY  
WITH THE SAME MOUNTED ON PRINTED CIRCUIT BOARD

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §§ 1.97 and 1.98**

Assistant Commissioner for Patents  
Washington, D.C. 20231

**RECEIVED**

**APR 12 2000**

TECHNOLOGY CENTER 2800

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the document which is listed on the attached Form PTO-1449 and/or listed herein and which the Examiner may deem relevant to patentability of the claims of the above-identified application.

1. U.S. Patent No. 5,011,066 issued April 30, 1991.

One copy of the listed document is submitted herewith.

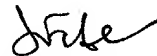
The present Information Disclosure Statement is being filed (1) no later than three months from the application's filing date or (2) before the mailing date of the first Office Action on the merits (whichever is later), and therefore no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

Yasuyoshi YAMADA  
Q56857  
INFORMATION DISCLOSURE STATEMENT

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a corresponding Foreign Search Report citing such document and indicating the degree of relevance found by the foreign office.

The submission of the listed document is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,



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